

Figure 1: Maximum average power dissipation versus average on state current

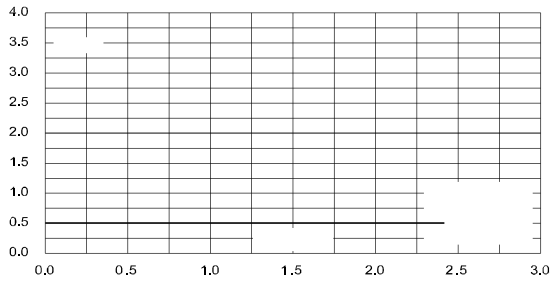


Figure 2: Average and DC on state current versus case temperature

Figure 3: Average and DC on state current versus ambient temperature (DPAK)

Figure 4: Relative variation of thermal impedance junction to ambient versus pulse duration (DPAK)

Figure 5: Relative variation of gate trigger current and holding current versus junction temperature

Figure 6: Relative variation of holding current versus gate-cathode resistance (typical values)